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SignatureDecember 6, 2005
DateIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/075,656 Confirmation No. 9370
Applicant : Detlev Richter
Filed : February 13, 2002
Title : Semiconductor Module with a Configuration for
the Self-Test of a Plurality of Interface
Circuits and Test Method
Group Art Unit : 2133
Examiner : John J. Tabone, Jr.
Docket No. : P2001,0097
Customer No. : 24131

R E S P O N S E under 37 C.F.R. § 1.116.

Mail Stop AF
Hon. Commissioner for Patents
P.O. Box 1450
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S i r :

Responsive to the Final Office Action dated September 6, 2005,
kindly consider the following:

Remarks/Arguments begin on page 2 of this paper.